Notice of References Cited Application/Control No. | Applicant(s)/Patent Under R examination YOO, WOO SIK Examiner | Art Unit | Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,211,041	04-2001	Ogura, Atsushi	438/458
*	В	US-6,313,014	11-2001	Sakaguchi et al.	. 438/475
*	С	US-5,281,543	01-1994	Fukuzawa et al.	438/186
*	D	US-5,141,894	08-1992	Bisaro et al.	438/479
*	Е	US-6,806,170	10-2004	Ward et al.	438/489
	F	US-			
	G	US-			
	Н	US-			
Ī.	ŀ	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-	<u> </u>		•

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Ν					
	0				·	
	Р					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

		•
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Stanley Wolf and Richard N.Tauber, Silicon Processing For The VLSI Era, 2000, Lattice Press, Second Edition, pages 259-261
	٧	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.